



A DOCPHOENIX

FOLLOW-ON DOCUMENT INDEX SHEET

## INCOMING

\_\_\_\_ ACPA \_\_\_\_  
Continuing Prosecution Application

\_\_\_\_ AP.B \_\_\_\_  
Appeal Brief

\_\_\_\_ C680 \_\_\_\_  
Request for Corrected Notice/Allowance

\_\_\_\_ C.AD \_\_\_\_  
Change of Address

\_\_\_\_ CFILE \_\_\_\_  
Request for Corrected Filing Receipt

\_\_\_\_ COCIN \_\_\_\_  
Papers filed re Certificate of Corrections

\_\_\_\_ CRFD \_\_\_\_  
Computer Readable Form Defective

\_\_\_\_ CRFE \_\_\_\_  
Computer Readable Form 'ENTERED'

\_\_\_\_ EABN \_\_\_\_  
Request for Express Abandonment

\_\_\_\_ ELC. \_\_\_\_  
Response to Election/Restriction

\_\_\_\_ IFEE \_\_\_\_  
Issue Fee Transmittal PTOL 85 B

\_\_\_\_ IRFND \_\_\_\_  
Refund Request

\_\_\_\_ L\_RIN \_\_\_\_  
Any Incoming to L&R

\_\_\_\_ N417 \_\_\_\_  
Copy of EFS Receipt Acknowledgement

\_\_\_\_ N/AP \_\_\_\_  
Notice of Appeal

\_\_\_\_ PA.. \_\_\_\_  
Change in Power of Attorney

\_\_\_\_ PC/I \_\_\_\_  
Power to Make Copies or to Inspect

\_\_\_\_ PEF. \_\_\_\_  
Pre-Exam Formalities Response

\_\_\_\_ PEFRRREISS \_\_\_\_  
Pre-Exam Formalities Reissue Response

\_\_\_\_ PEFRSEQ \_\_\_\_  
Pre-Exam Formalities Sequence Reply

## INCOMING

\_\_\_\_ LET. \_\_\_\_  
Misc. Incoming Letter

\_\_\_\_ IMIS \_\_\_\_  
Miscellaneous Internal Document

\_\_\_\_ PGEA \_\_\_\_  
Req Express Aband to avoid Publication

\_\_\_\_ PGA9 \_\_\_\_  
Req for Corrected Pat App Publication

\_\_\_\_ PGREF \_\_\_\_  
Req for Refund of Publication Fee Paid

\_\_\_\_ PROTEST \_\_\_\_  
Protest Documents Filed by 3<sup>rd</sup> Party

\_\_\_\_ PROTRANS \_\_\_\_  
Translation of Provisional in Nonprovisional

11/12/2003 REM 2  
\_\_\_\_  
Applicant Remarks in Amendment

\_\_\_\_ RESC \_\_\_\_  
Rescind Non-Publication Request

\_\_\_\_ RETMAIL. \_\_\_\_  
Mailed Returned by Post Office

\_\_\_\_ XT/I \_\_\_\_  
Extension of Time filed separate

## APPL PARTS

\_\_\_\_ 371P \_\_\_\_  
PCT Papers in a 371 Application

\_\_\_\_ A... \_\_\_\_  
Amendment Including Elections

\_\_\_\_ A.NE \_\_\_\_  
After Final Amendment

\_\_\_\_ A.PE \_\_\_\_  
Preliminary Amendment

\_\_\_\_ ABST \_\_\_\_  
Abstract

\_\_\_\_ ADS \_\_\_\_  
Application Data Sheet

\_\_\_\_ AF/D \_\_\_\_  
Affidavit or Exhibit Received

\_\_\_\_ APPENDIX \_\_\_\_  
Appendix

## APPL PARTS

\_\_\_\_ ARTIFACT \_\_\_\_  
Artifact

\_\_\_\_ CLM \_\_\_\_  
Claim

\_\_\_\_ COMPUTER \_\_\_\_  
Computer Program Listing

\_\_\_\_ CRFL \_\_\_\_  
CRF Transfer Request

\_\_\_\_ CRFS \_\_\_\_  
Computer Readable Form Statement

\_\_\_\_ DIST \_\_\_\_  
Terminal Disclaimer Filed

\_\_\_\_ DRW \_\_\_\_  
Drawings

\_\_\_\_ FOR \_\_\_\_  
Foreign Reference

\_\_\_\_ FRPR \_\_\_\_  
Foreign Priority Papers

\_\_\_\_ IDS \_\_\_\_  
IDS Including 1449

\_\_\_\_ NPL \_\_\_\_  
Non-Patent Literature

\_\_\_\_ OATH \_\_\_\_  
Oath or Declaration

\_\_\_\_ PET. \_\_\_\_  
Petition

\_\_\_\_ PGPUB DRAWINGS \_\_\_\_  
Box PG Pub Drawings

\_\_\_\_ SEQLIST \_\_\_\_  
Sequence Listing

\_\_\_\_ SPEC \_\_\_\_  
Specification

\_\_\_\_ SPEC NO \_\_\_\_  
Specification Not in English

6/26/03

REMARKS

Claims 1-22 are pending in the present application with all claims being rejected in the present Final Office Action.

Claims 1-6, 12-16, 21, and 22 were rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,946,257 (Keeth). Claims 7-11 and 17-20 were rejected under 35 U.S.C. §103(a) as being unpatentable over Keeth in view of U.S. Patent No. 6,496,027 (Sher).

Keeth describes a memory circuit, which includes distributed voltage generators to selectively provide power to memory arrays of the memory circuit.

Contrary to the Examiner's assertion, the text in column 9, lines 60-65 of Keeth reading:

"This method of array control provides the benefit of flexible fabrication. That is, as integrated circuits become bigger, power distribution becomes more difficult. The requirements of a single voltage generator for an entire circuit would be impractical to meet. Thus, using distributed voltage generators provides a more efficient manner of adjusting an integrated circuit than a series of isolation switches"

does not disclose "at least one enable/disable circuit for selectively connecting and disconnecting at least a portion of the respective macro from the integrated circuit system" recited in Claim 1 as amended or "means for controlling the means coupled to the means for receiving the external voltage according to an enable/disable signal to selectively connect and disconnect at least a portion of the respective macro of the plurality of macros" recited in Claim 12 as filed.

By stating in column 9, lines 64-67: "Thus, using distributed voltage generators provides a more efficient manner of adjusting an integrated circuit than a series of isolation switches" Keeth teaches away from the above stated recitations of independent Claims 1 and 12.

Without conceding the patentability per se of dependent Claims 2-5, 12-16, 21, and 22, it is submitted that they overcome Keeth by virtue of their dependencies on independent Claims 1 and 12. Furthermore, without conceding the patentability per se of dependent Claims 7-11, and 17-20, it is submitted that they overcome Keeth, Sher, and the combination thereof by virtue of their dependencies on independent Claims 1 and 12. Accordingly it is submitted that Claims 1-5, 12-16, 21, and 22 are patentable over Keeth and Claims 7-11, and 17-20 are patentable over Keeth in view of Sher.